

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,992,919 B2
APPLICATION NO. : 10/731732
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INVENTOR(S) : Andrei et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In line 3 of claim 9 (column 31, line 53) change “stacked wit” to --stacked with --.

In line 4 of claim 14 (column 32, line 7) delete “giant” after “exhibiting”.

In line 4 of claim 17 (column 32, line 24) change “frat” to --first--.

In line 16 of claim 23 (column 33, line 12) change “electrodes” to --electronics--.

In line 24 of claim 23 (column 33, line 20) change “tin-film” to --thin-film--.

In line 4 of claim 27 (column 33, line 48) change “modifies” to --modules--.

In line 1 of claim 28 (column 33, line 50) change “memory ray” to --memory array--.

In line 2 of claim 37 (column 34, line 30) change “components art” to --components on--.

In line 1 of claim 40 (column 34, line 2) change “memory any” to --memory array--.

Signed and Sealed this

Third Day of October, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office